



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Reithinger, *et al.* Docket No.: 00P7685US
Serial No.: 09/596,129 Art Unit: 2814
Filed: June 16, 2000 Examiner: Phat X. Cao
Title: Multiple Chip Semiconductor Arrangement having Electrical Components in
Separating Regions

Mail Stop RCE
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's
Office Action mailed November 12, 2003. Please amend the above-referenced application as
follows. This amendment does not raise any new issues.

02/17/2004 AJM:W351 00000001 501005 00000001
01 FC:1201 602.00 DA